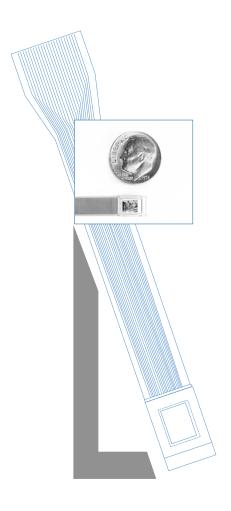
# Cyber Display<sup>®</sup>





VGA Electronic Viewfinder Module (VGA LVD EVF)

KCD-VMCD-ITT

Delivery Specification

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#### 1 General Information

### 1.1 Product Description

This specification describes an Electronic Viewfinder Module (EVF) consisting of a 0.26" VGA LVD ( $640 \times 480 \times 3$  color dot resolution) color filter display, white LED backlight, lens set with diopter adjustment and housing.

#### 1.2 Product Numbers

KCD-VMCD-ITT: EVF with Dial Base

#### 1.3 Other Specifications

VGA LVD EVF module specification PS-0272 takes precedence over any other specifications; refer to VGA LVD display specification PS-0266 for additional display related information.

#### 1.4 Conflict Resolution and New Matters

In the cases of items not covered by this specification, or interpretations of the specification: Kopin will work in good faith to resolve the conflict to mutual satisfaction.

# 2 Product Specification

#### 2.1 General Construction

The VGA LVD EVF consists of a VGA LVD Cyberdisplay<sup>®</sup> LCD display and backlight module, lens set, plastics housing, diopter adjustment dial, and cover glass. Part number KCD-VMCD-ITT includes a focus dial adaptor.

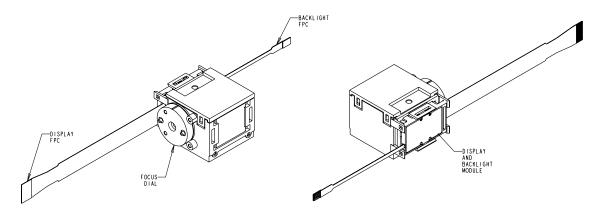


Figure 2-1: EVF with Dial Base, p/n KCD-VMCD-ITT

# 2.2 General Specification

**Table 2-1: General Module Attributes** 

Item	Specification
Display Active Resolution	640 x 480 x 3 (VGA)
Active Area	5.376 mm x 4.032 mm
Diagonal Size	0.26" (6.72 mm)
	2.8 µm x 8.4 µm dot pitch (8.4 µm x 8.4 µm square
Dot Pitch	pixels)
Color Configuration	Vertical Stripe
Field of View (FOV)	23.8 degrees (Diagonal)
Overall Dimensions	20 mm (W) x 18 mm (H) x 25 mm (D)
Overall Weight	6.4 g

# 2.3 RoHS Compliance

The VGA LVD EVF, including all subcomponents, complies with all RoHS requirements.

# 3 Electrical Characteristics

3.1 Pin Out: Interface Pin List (Reference VGA LVD Display Specification PS-0266 for detailed descriptions)

**Table 3-1: Interface Pin List** 

Pin	Symbol	Description
1	VCOM	Pixel common electrode = 0V
2	VEE	Supply = 0V
3	VDD	Supply
4	VSS	Supply
5	CK0	Clock
6	CK1	Clock
7		Instruction input
8	HS	Horizontal start
9	DWN	Vertical scan control
10	EN	Row enable
11	SLEEP*	Sleep mode
12	VIDRH	High red video input
13	VIDGH	High green video input
14	VIDBH	High blue video input
15	VIDRL	Low red video input
16	VIDGL	Low green video input
17	VIDBL	Low blue video input
18	TOUT1	Test output
19	TOUT2	Test output
20	VCOM	Pixel common electrode = 0V
21	RESV	Reserved (Tied to Pin 20)
22	RESV	Reserved (Tied to Pin 20)
23	RESV	Reserved (Tied to Pin 20)

<sup>\*</sup> Signal is active low

# 3.2 Display Absolute Maximum Ratings

**Table 3-2: Display Absolute Maximum Ratings** 

Parameter/Condition	Symbol	Min	Max	Units
Supply voltage source	$V_{DD}$	- 0.5	6.0	V
Supply voltage – sink	$V_{SS}$	- 7.0	0.5	V
All inputs	VI	V <sub>SS</sub> -0.5	V <sub>DD</sub> + 0.5	V

Note: All voltages relative to  $V_{EE} = 0$ .

# 3.3 LED Absolute Maximum Ratings

Table 3-3: LED Absolute Maximum Ratings T<sub>a</sub>=25°C

Parameter/Condition	Symbol	Absolute Maximum	Units
LED Forward Current	$I_f$	30	mA
LED Reverse Voltage	$V_{r}$	5	V
LED Power Dissipation	$P_d$	102	mW

# 3.4 Typical Electrical Drive Conditions

**Table 3-4: Typical Electrical Drive Conditions** 

Parameter/Condition	Symbol	Тур	Max	Units
LED Forward Current	$I_f$	18	20	mA
LED Forward Voltage	$V_{f}$	3.1	3.4	V
Display	Reference	e VGA LVD S	Specification	PS-0266

# 4 Optical Characteristics

# 4.1 Display/Backlight Optical Characteristics

Optical characteristics described in section 4.1 apply to the display and backlight module only. Actual EVF lens module performance may differ somewhat due to the effects of the optical lens system.

Table 4-1: Optical Characteristics T<sub>a</sub>=25°C

Table 4 1: Option Characteristics								
Item			Symbol	Notes	Min	Тур	Max	Unit
Contrast ratio	Vsig = 0.0 + 4	.0V	CR <sub>4.0</sub> 25	1	80	110		
	W	Х	Wx	2	0.270	0.310	0.350	
		Υ	Wy		0.280	0.320	0.360	
	R	Х	Rx		0.520	0.560	0.600	1
Chromaticity		Υ	Ry		0.280	0.320	0.360	CIE
Cilionialicity	G	Х	Gx		0.250	0.290	0.340	standards
		Υ	Gy		0.530	0.570	0.610	1
	В	Х	Bx		0.120	0.160	0.200	
		Υ	Ву		0.060	0.100	0.140	1
V-T	V <sub>90</sub>	25°C	V <sub>90-25</sub>	3	0.4	0.7	1.0	
characteristics	V <sub>50</sub>	25°C	V <sub>50-25</sub>		1.1	1.5	1.8	V
Characteristics	V <sub>10</sub>	25°C	V <sub>10-25</sub>		1.7	2.3	2.8	
Response time	ON time	25°C	ton <sub>25</sub>	4		5	15	ms
Response une	OFF time	25°C	toff <sub>25</sub>			30	40	1115
Flicker			F	5			-30	dB
Brightness Uniformity			BU	6	70	85		%
Center Brightness (If = 18mA) $\theta$ =0			L	7	220	350		cd/m <sup>2</sup>

#### **Notes On Measurement Conditions:**

- 1. CR<sub>4.0</sub>25 = (Luminance White)/(Luminance Black). System I
- 2. CIE Standard 1931. Color coordinate measurement allowance is ± 0.01. System I
- 3. V-T is relationship of signal amplitude to transmittance. System I
- 4. VESA 2.0 standard. System I + Oscilloscope
- 5. 20log(AC/DC) @ 50% transmittance. System I + Spectrum Analyzer
- 6. BU=Min/Max x 100%. Measured at 5 points of powered display module with all white image.
- 7. Measured at the center of powered display under proper image condition. System I.
- 8. All measurements taken from display module. EVF module performance may vary due to effects of lens system.

System I: Equipment: PR-670 @ 2 mm spot size

#### **EVF Optical Parameters** 4.2

**Table 4-2: EVF Optical Parameters** 

Item	-	Specification
Field of View (1)	Diagonal	23.8 Degrees
	Horizontal	19.0 Degrees
	Vertical	14.3 Degrees
Virtual Image Size (2)	Diagonal	421 mm
	Horizontal	334 mm
	Vertical	250 mm
Eye Relief (3)		17.4 mm
Focus Range, Diopter (4)		-3.0D ~ +1.0D
Resolution		> 32 line/mm
Eye Relief (From Cover Glass)		15.4 mm
TV Distortion		+/-1.2%

- +/-5%
- At 1m focus distance
- From vertex of front lens
- Guaranteed range. Actual range may be larger

# 5 Reliability Items

#### **Environmental Specifications** 5.1

**Table 5-1: Environmental Specifications** 

Parameter	Condition	Notes
Low Temperature Storage	-20°C, 240hrs	(1)
High Temperature Storage	+60°C,240hrs	(1)
Low Temperature Operation	0°C, 240 hrs	(1)
High Temperature Operation	+60°C, 240 hrs	(1)
Humidity	40°C 80%RH, 240hrs	(1)
Shock	70 mm on Concrete, 5 times	(2)
Vibration	5 Hz – 60 Hz – 5 Hz, 30 min, 5 cycles all 3 axes	(1)
ESD (Human Model)	500 V Electric Discharge	(3)
Focus Dial Durability	1500 cycles, full range back to front	

- (1) Criteria: Pass visual appearance criteria after test
   (2) Criteria: Pass visual appearance criteria after test (excluding scratches)
   (3) LCD only

#### 6 Mechanical Data

#### 6.1 Focus Mechanism

**Table 6-1: Environmental Specifications** 

Item		Specification
Focus Adjustment	One Click (~0.4D)	12 degrees
	Full Rotation	180 degrees
	Rotation Force	0.25Ncm ~ 0.5Ncm
	Dial Backlash, Radial	=2 degrees</td
	Dial Backlash, Thrust	=0.1mm</td

## 6.2 Display Flex Cable Interconnect

The flexible FPC cable has 23 pins with a 0.3 mm contact pitch. The FPC is designed to be compatible with specifications for Tyco (1746237-7), JST (23FXR-RSM1-GAN-TB), or equivalent connectors.

The flexible FPC cable is strain relieved, but tugging forces should be limited to less than 0.5 kg perpendicular to the display and less than 1 kg parallel to the display. The minimum inside bend radius for the cable is 0.8 mm. Repeated reforming is not recommended.

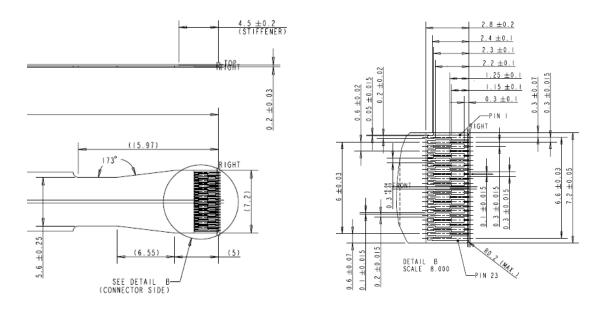
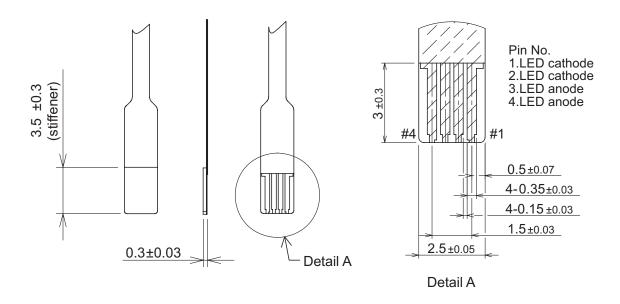


Figure 6-1: Display FPC Interconnect

# 6.3 Backlight Interconnect

The flexible FPC cable has 4 pins with a 0.5 mm contact pitch. The FPC is designed to be compatible with specifications for JST (04FHSY-RSM1-GAN-TB) or equivalent connectors.



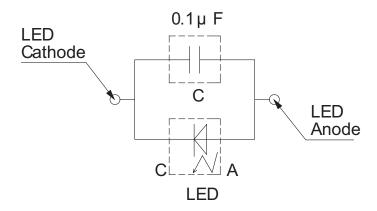


Figure 6-2: Backlight Connection

# 6.4 Mechanical Outline Drawings

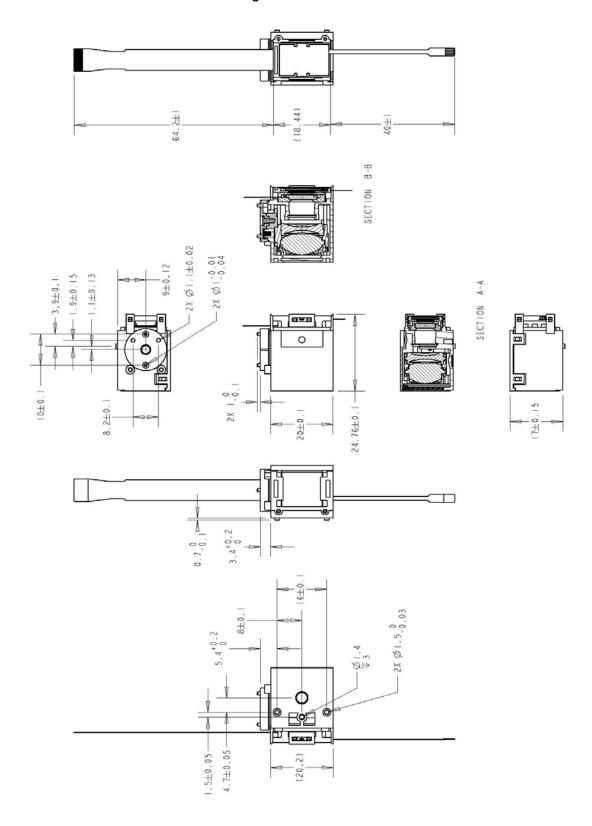


Figure 6-3: Mechanical Layout KCD-VMCD-ITT

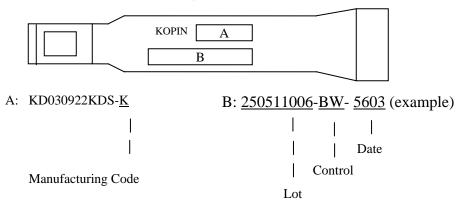
# 7 Packaging Specification

#### 7.1 Lot Identification

Lot code markings are separately used for the major EVF components (LCD, backlight, lens module). These individual codes are shown below:

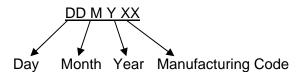
#### LCD Lot Code

Each LCD has an identification code printed on the display flex cable. The identification code can be used for LCD traceability.



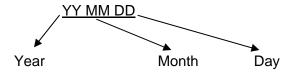
#### Backlight Lot Code

Each backlight has a lot identification code stamped on the metal back cover. The lot numbering is



#### Lens Module Lot Code

Each lens module has a lot identification code stamped on the lens module cover. The lot numbering is



## 7.2 Module Shipping Package

Modules are assembled and tested in a clean room environment. Modules are packaged in antistatic plastic trays. Each tray holds 100 modules maximum. The trays stack on top of each other, with the bottom of one tray being the top of the one underneath. The trays are stacked 5 deep. One empty tray is placed on top of the stack as a cover. Each stack of trays is wrapped in an antistatic plastic bag. A fully populated bag contains up to 500 Modules. Each bag has an identification Bag Label attached. Each box contains a maximum of 1000 modules. See Figure 7-1.

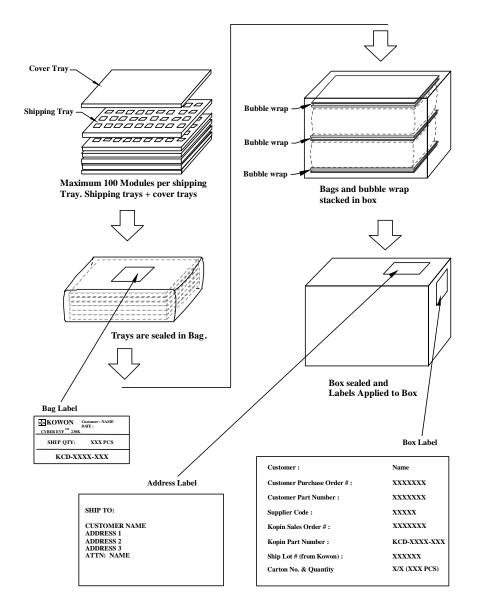
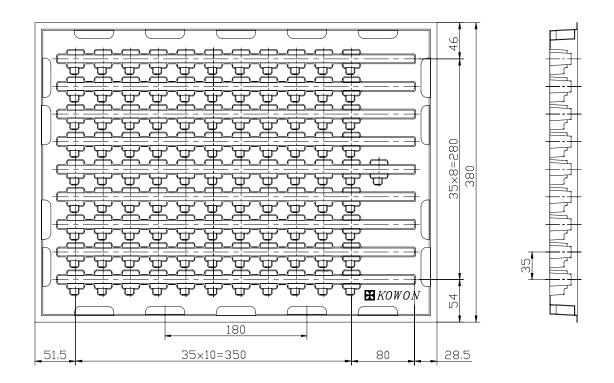


Figure 7-1: Shipping Package (Typical)



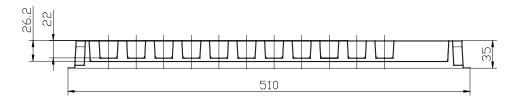


Figure 7-2: Shipping Tray (Dimensions in mm)



Figure 7-3: Loading of modules in Shipping Tray (Example)

# 8 EVF Appearance

# 8.1 Display Appearance

Display Appearance is based upon Kopin Standard Test FAT driving conditions.

**Table 8-1: Display Appearance Specification** 

Defect Type	Defect Criteria			
Image	l or faded image, no obvious streaking or shadow, e, no alternating columns or rows			
Line	No lines out  Very faint lines allowable based on limit samples (maximum 1)			
Pixel	See Table 8-2			
Adjacent pixels		Same criteria as for pixel defects, based on aggregate brightness (See Table 8-2) Not allowed		

**Test Conditions:** Test board runs in row inversion at 60 Hz frame rate. Visual focal plane is on the pixel array. Limit **Samples:** Limit samples to be used for pixel and other cosmetic defects as required.

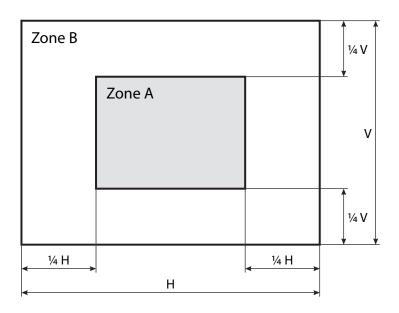


Figure 8-1: Defect Zone Definition

**Table 8-2: Defect Allowance** 

ZONE	DEFECT	NUMBER	
А	S or M	2	
	L	1	
В	S or M	4	
	L	1	
A + B	S, M or L	Max 7	

#### Where:

Small (S) defect differs from nominal pixel brightness by 10–25%. Medium (M) defect differs from nominal pixel brightness by 25–75%. Large (L) defect differs from nominal pixel brightness by  $\geq$  75%. Defect can be black or white spot.

#### 8.2 Lens Appearance

Lens surface quality shall be the equivalent of 40-20 scratch-dig per Mil-0-13830. This is interpreted as follows:

- Maximum scratch number shall be 40 (i.e., 40 um in width).
- For multiple scratches, sum of all scratch length x (scratch number / 40) is less than one lens diameter.
- Maximum dig number shall be 20 (i.e., 200 um in diameter).
- Non-circular digs shall be measured by (length + width)/2.
- Sum of all dig diameters shall be less than 400 um.

### 8.3 Housing Appearance

The housing shall have no excessive voids, cracks, chipping, scratches, warpage, or contamination that affect the function of the unit.

#### 8.4 Focus Control Movement

The focus dial shall rotate smoothly and have a clear feeling of clicks. No unstable move, jump, or shaking of the image shall be detected during focus adjustment.

# 9 Handling Precautions

## 9.1 EVF Handling

The EVF module should be handled in a clean environment only. Filtered, Ionized air should be used to blow dust particles off of the cover glass as required. The LCD display and Backlight LED can be damaged by static electricity charge. Use standard static control procedures when handling the EVF module. Static control methods should include the use of static dissipative table and floor mats, static dissipative gloves, antistatic lab coat, and ESD ground straps.